

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Uneme, Masakatsu

Serial No.: 10/657,464

Filed: September 8, 2003

Title: (As Amended) Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits

Attorney Docket No.: N26532602E

Group Art Unit: 2186

Examiner: Kim, Hong Chong

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RESPONSE TO OFFICE ACTION

Mail Stop Amendment

5 Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

10 A. Introductory Comments

The following is submitted in response to the Office Action dated September 21, 2005, and is currently due December 21, 2005.

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37 C.F.R. §1.8

I hereby certify that this correspondence is being

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[X] transmitted via facsimile to the United States Patent and Trademark Office to fax number: 1-571-273-8300Date of Transmittal: December 16, 2005

[] deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Date of Deposit: _____

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